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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent application of:

Alexander Lidow et al.

March 20, 1992

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Group Art Unit: 2508

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For: PLURAL POLYGON SOURCE PATTERN FOR MOSFET

Hon. Commissioner of Patents and Trademarks
Washington, DC 20231

Attention: Official Draftsman

FORMAL DRAWINGS

Sir:

Responsive to Paper No. 9/B attached to the Notice of Allowance dated February 24, 1992, enclosed herewith please find 3 sheets of formal drawings containing Figures 1 through 5 for the above-identified application.

Respectfully submitted,

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on March 20, 1992

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Name of applicant, assignee or
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Date of Signature

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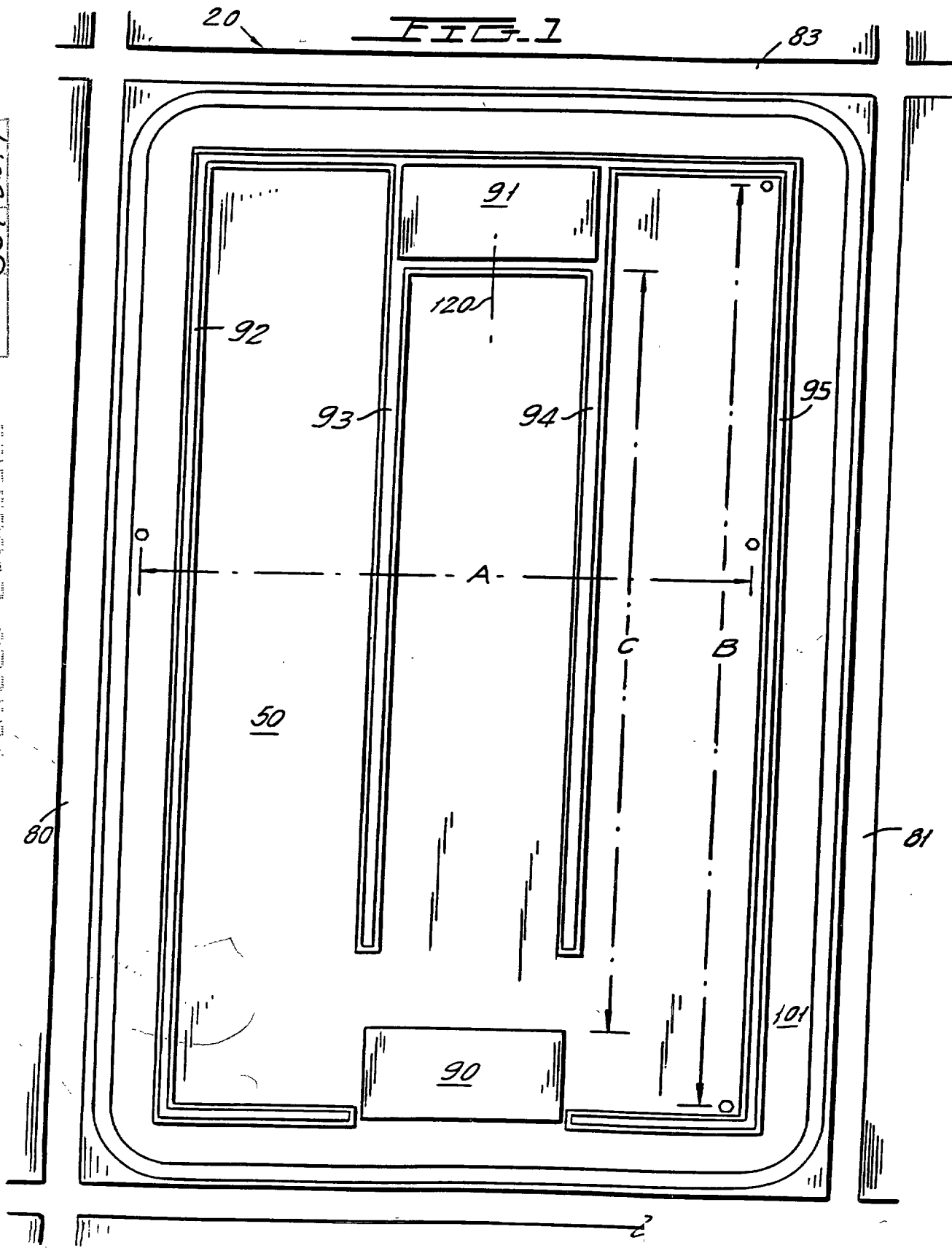
653,017

06 FIG 2
357 23.4

20

FIG. 1

83



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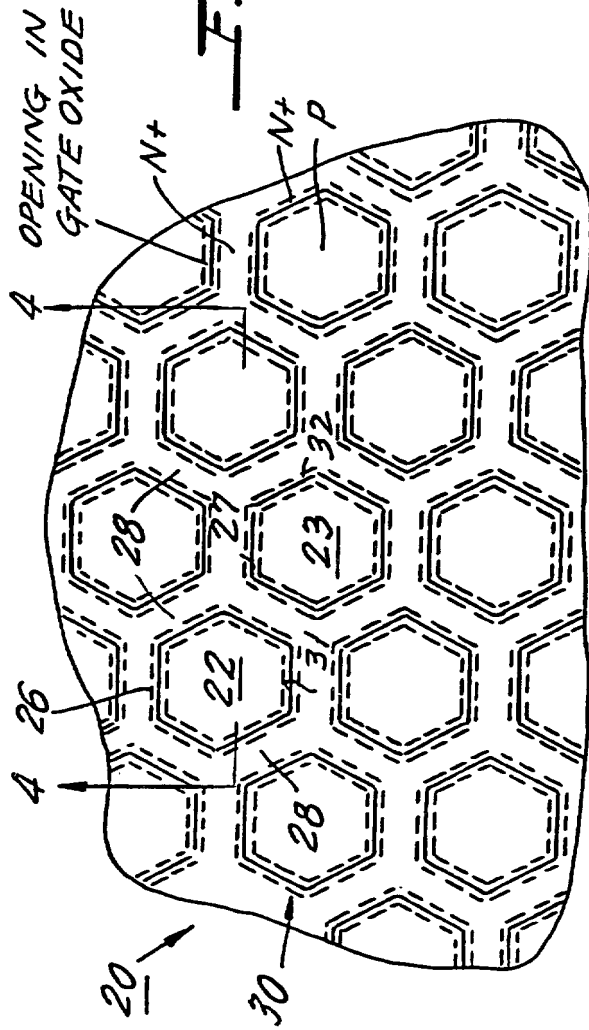


FIG. 3

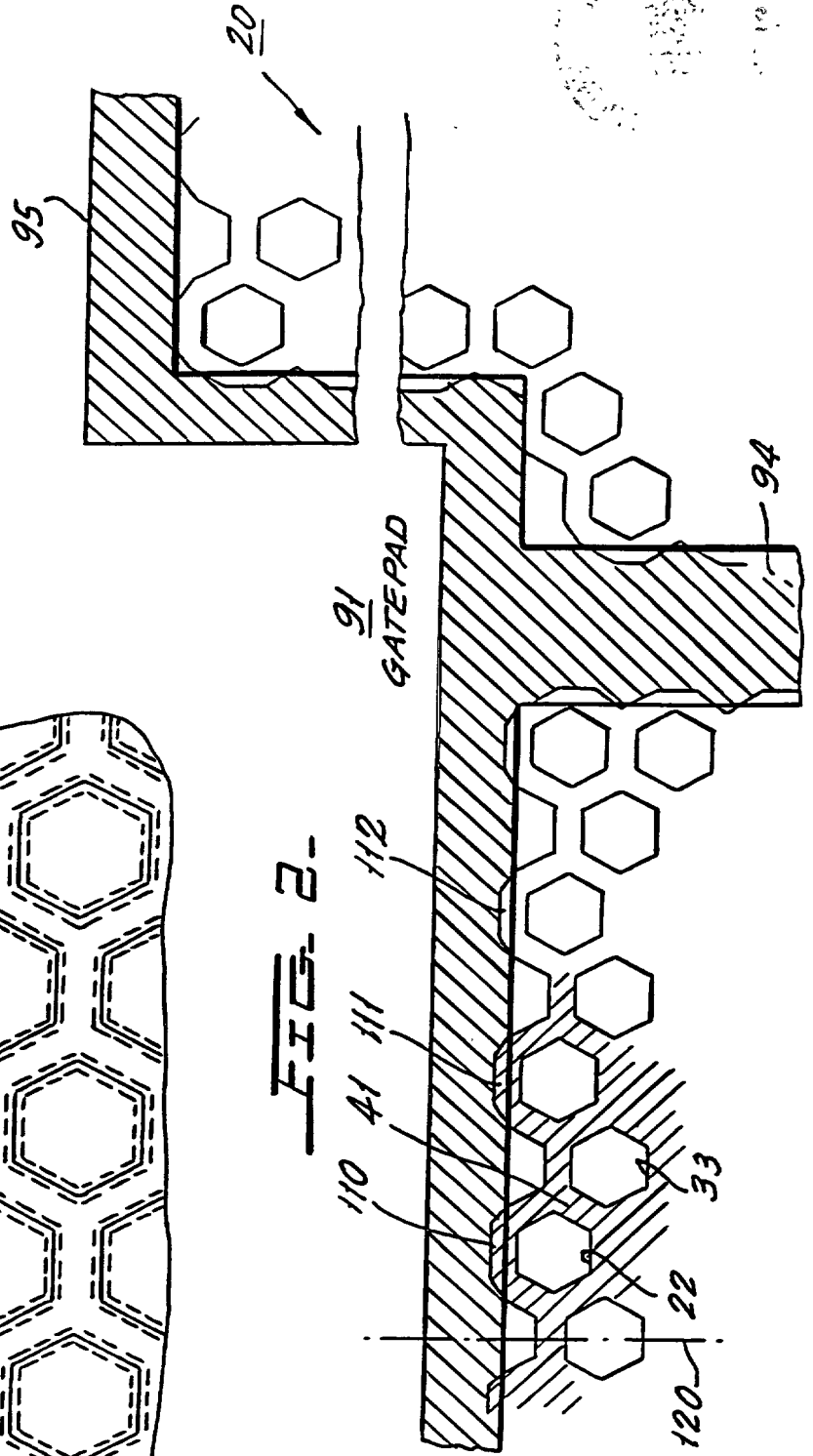


FIG. 2

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